



hMETIS Version 1.5

A software package for partitioning large hypergraphs, especially those arising in circuit design.

IP Status: Trademarked

Applications

- Very large scale integration (VLSI) design
- Data-mining
- Efficient storage of large databases on disks
- Transportation management

Technology Overview

hMETIS is a set of programs for partitioning hypergraphs such as those corresponding to VLSI circuits. The algorithms implemented by hMETIS are based on the multilevel hypergraph partitioning scheme. hMETIS provides high-quality partitions extremely fast.

Phase of Development

TRL: 9

Broadly implemented, with over 1900 citations. This is a non-open, fee-based version of METIS (<https://github.com/KarypisLab/METIS>) built specifically for hypergraph partitioning.

Desired Partnerships

This technology is now available for:

- License
- Sponsored research
- Co-development

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Researchers

- [George Karypis, PhD](#) Professor, Department of Computer Science & Engineering

References

1. G. Karypis; R. Aggarwal; V. Kumar; S. Shekhar(1999) , <https://ieeexplore.ieee.org/document/748202/authors#authors>, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 7, 69-79
2. George Karypis and Vipin Kumar(1998) , <https://conservancy.umn.edu/bitstreams/737f4eb7-65f4-45e0-89c5-9ae3327f3bfa/download>, Technical Report TR 98-036, Department of Computer Science, University of Minnesota

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